

Claims:

1. A programmable logic device with high speed serial interface circuitry comprising:
 - programmable logic circuitry;
 - high speed serial interface circuitry, portions of which can selectively be coupled to portions of the programmable logic circuitry; and
 - a plurality of channels of functional sub-circuits for selectively coupling portions of the programmable logic circuitry and portions of the high speed serial interface circuitry.